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** CONTINUING DATA **********************************						
** FOREIGN APPLICATIONS ************************************						
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** SMALL ENTITY ** ** 08/04/2004						
Foreign Priority claimed 35 USC 119 (a-d) condition	ns yes no Met aft	STATE OR	SHEETS	TOTA	L INDEPENDENT	
met Verified and Acknowledged	Allowance Market	COUNTRY CA	DRAWING 219	CLAIM 1	S CLAIMS	
ADDRESS 34036 SILICON VALLEY PATENT GROUP LLP 2350 MISSION COLLEGE BOULEVARD SUITE 360 SANTA CLARA, CA 95054						
TITLE Modular bipolar-CMOS-DMOS analog integrated circuit and power transistor technology						
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